

ABSTRACT OF SAID DISCLOSURE

It is possible to provide a data-processing system wherein a single device such as a microprocessor is capable of selectively making an access to one of high-speed and low-speed devices each operating synchronously with a clock signal peculiar to the device, and clock control can be executed with ease in an operation to switch the external device to be accessed from one to another. Clock signals each having a required frequency are provided individually through separate clock wires to high-speed and low-speed devices to be accessed by a microprocessor. Since control is executed to switch a synchronous clock signal of an external bus interface control circuit embedded in the microprocessor in accordance with an external device or an address area being accessed by the microprocessor, there is exhibited an effect of easy clock control in an operation to switch the external device to be accessed from one to another without the need to switch the clock signal itself which is to be supplied to the external device.

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